**Digital Design Using VHDL**

(Practical design oriented training)

##### Introduction to VHDL - 3 hrs

**[**Code structure, Data Types, Signals & variables, Operators & Attributes, Packages, Components, Functions, Procedures, PLDs]

##### Concurrent code (with programming examples) - 3 hrs

* Sequential design (with programming examples) - 3 hrs
* State Machine (with programming examples) - 3 hrs